

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
Michinari YAMANAKA : Confirmation Number:
Serial No.: : Group Art Unit:
Filed: March 24, 2004 : Examiner:
For: DRY ETCHING APPARATUS AND DRY ETCHING METHOD

INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

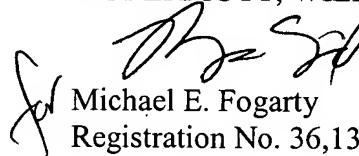
The relevance of the six references listed on attached Form 1449 is discussed in the present specification.

Serial No.:

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)			ATTY. DOCKET NO. 60188-809		SERIAL NO.	
			APPLICANT Michinari YAMANAKA			
			FILING DATE March 24, 2004		GROUP	
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ₂ (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	US					
	US					
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FOREIGN PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Code ₃ -Number-Kind Codes (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation
						Yes
	A	2002-009042	01/11/2002	SEMICONDUCTOR LEADING EDGE TECHNOLOGIES INC		English Abstract (w/ Japanese Patent No. 3333177)
		07-245292	09/19/1995	TOKYO ELECTRON LTD TOSHIBA CORP		Japanese (w/ English Abstract)
		08-186096	07/16/1996	FUJITSU LTD FUJITSU VLSI LTD		Japanese (w/ English Abstract)
	A	P2002-09048	01/11/2002	MATSUSHITA ELECTRIC IND CO LTD		Japanese (w/ English Abstract)
	A	P2002-164323	06/07/2002	SEMICONDUCTOR LEADING EDGE TECHNOLOGIES INC		Japanese (w/ English Abstract)
	A	P2002-190466	07/05/2002	SEMICONDUCTOR LEADING EDGE TECHNOLOGIES INC		Japanese (w/ English Abstract)
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
EXAMINER			DATE CONSIDERED			

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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.